

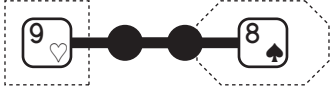
Circuit Constraints 40-52 (Test 2 Reward)

Dr. When comments:

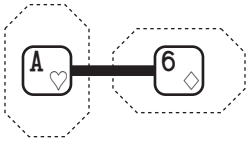
"Be careful around Constraint 44... the direct route might be three hops, but some indirect routes have three hops too!"



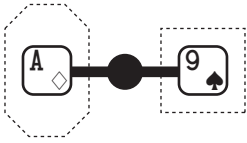
40. The Dimension Neonine Circuit should be **due south** of the Dimension Threesistor.



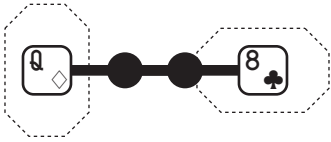
41. The Hartley Neonine Circuit should be **three hops away** from the Spartan Freight Chip.



42. The Hartley Acentuator should be **one hop away** from the Dimension Transixtor.



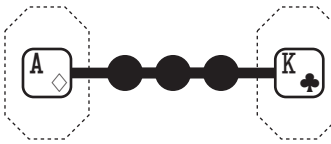
43. The Dimension Acentuator should be **two hops away** from the Spartan Neonine Core.



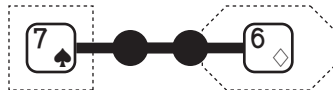
44. The Dimension Queenistor should be **three hops away** from the Cluster Freight Chip.



45. The Cluster Freight Chip should be **four hops away** from the Hartley Freight Chip.



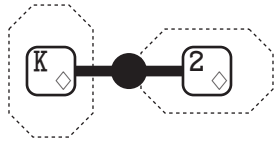
46. The Dimension Acentuator should be **four hops away** from the Cluster Sparking Node.



47. The Spartan Sevenar Circuit should be **three hops away** from the Dimension Transixtor.



48. The Cluster Rectifier should be **four hops away** from the Hartley Enforcer.



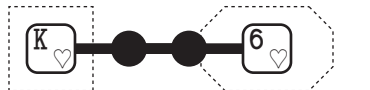
49. The Dimension Sparking Node should be **two hops away** from the Dimension Twopacitor.



50. The Cluster Rectifier should be **four hops away** from the Dimension Twopacitor.



51. The Hartley Enforcer should be **three hops away** from the Hartley Twopacitor.



52. The Hartley Sparking Node should be **three hops away** from the Hartley Transixtor.

1. K♥-5♦

2. A♠-5♣

3. A♠-4♣

4. ♠V-♠N-♦J

5. J♦-3♠

6. Q♥-7♦

7. ♣V-♠N-♣8

8. ♠2-♠XZ-♠3

9. ♣∞-♠+Z-♠N

10. Q♣-3♠

11. ♠8-♠XZ-♠4

12. 4♦-3♠

13. J♣-7♦

14. Q♠-7♣

15. 9♣-7♦

16. 3♣-2♣

17. A♠-3♣

18. ♠J-♠XZ-♠2

19. ♠7-♠XZ-♠5

20. 5♥-2♠

21. ♠J-♠XZ-♠4

22. J♣-10♥

23. K♠-6♦

24. 6♣-♠N-6♦

25. Q♠-6♣

26. 6♠-5♥

Constraints 1-26 repeated here for your convenience.