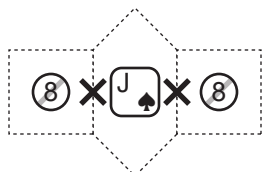
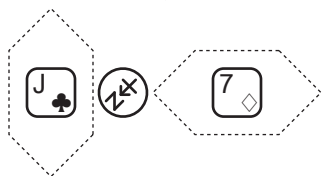


Constraints, part 0

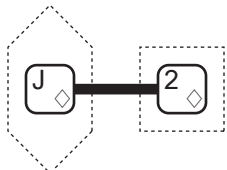
The diagrams to the left of each constraint depict that constraint in graphical form. There is no extra information in them.



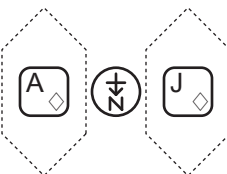
0. The Spartan Input Jack should be **next to** the empty Freight Chip node, but there should be no direct connection between them.



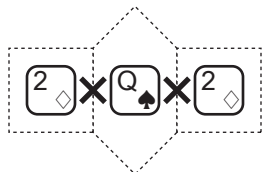
1. The Cluster Input Jack should be **directly northeast** of the Dimension Sevenar Circuit.



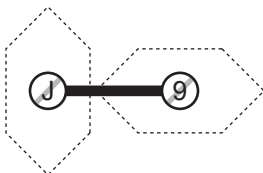
2. The Dimension Input Jack should be **one hop away** from the Dimension Twopacitor.



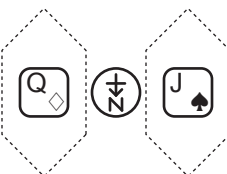
3. The Dimension Acentuator should be **directly east** of the Dimension Input Jack.



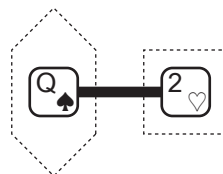
4. The Spartan Queenistor should be **next to** the Dimension Twopacitor, but there should be no direct connection between them.



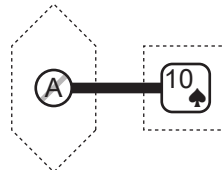
5. The empty Input Jack node should be **one hop away** from the empty Neonine Core node.



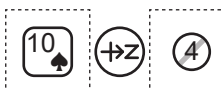
6. The Dimension Queenistor should be **directly east** of the Spartan Input Jack.



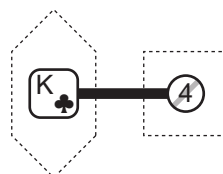
7. The Spartan Queenistor should be **one hop away** from the Hartley Twopacitor.



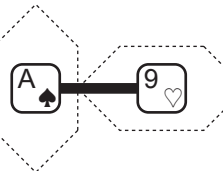
8. The empty Acentuator node should be **one hop away** from the Spartan Tensor Module.



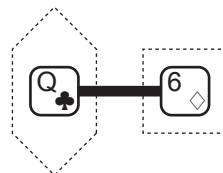
9. The Spartan Tensor Module should be **directly south** of the empty Enfourcer node.



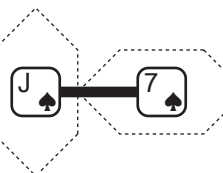
10. The Cluster Sparking Spot should be **one hop away** from the empty Enfourcer node.



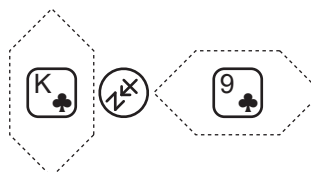
11. The Spartan Acentuator should be **one hop away** from the Hartley Neonine Core.



12. The Cluster Queenistor should be **one hop away** from the Dimension Transixtor.



13. The Spartan Input Jack should be **one hop away** from the Spartan Sevenar Circuit.



14. The Cluster Sparking Spot should be **directly northeast** of the Cluster Neonine Core.