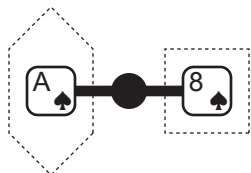


# Constraints, part 3

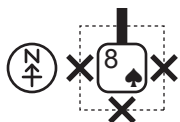
You should receive this list after passing Test 3.



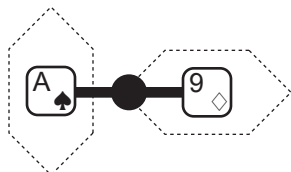
45. The Dimension Freight Chip should be **directly south** of the Spartan Rectifier.



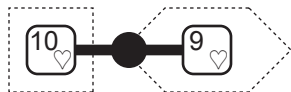
46. The Spartan Acentuator should be **two hops away** from the Spartan Freight Chip.



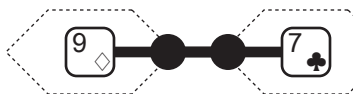
47. The Spartan Freight Chip should have **only one connection**, and that connection should go **north**.



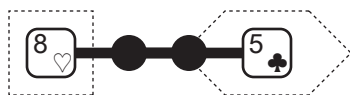
48. The Spartan Acentuator should be **two hops away** from the Dimension Neonine Core.



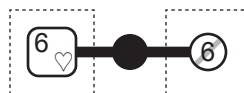
49. The Hartley Tensor Module should be **two hops away** from the Hartley Neonine Core.



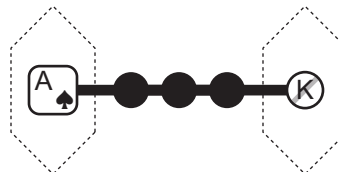
50. The Dimension Neonine Core should be **three hops away** from the Cluster Sevenar Circuit.



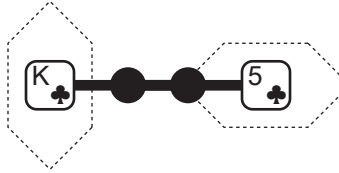
51. The Hartley Freight Chip should be **three hops away** from the Cluster Rectifier.



52. The Hartley Transixtor should be **two hops away** from the empty Transixtor node.



53. The Spartan Acentuator should be **four hops away** from the empty Sparking Spot node.



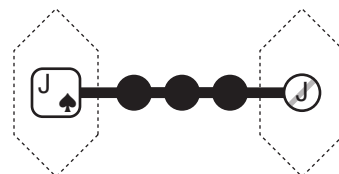
54. The Cluster Sparking Spot should be **three hops away** from the Cluster Rectifier.



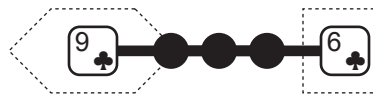
55. The empty Enfoucer node should be **four hops away** from the Hartley Threesistor.



56. The Dimension Freight Chip should be **four hops away** from the Hartley Transixtor.



57. The Spartan Input Jack should be **four hops away** from the empty Input Jack node.



58. The Cluster Neonine Core should be **four hops away** from the Cluster Transixtor.



59. The empty Freight Chip node should be **three hops away** from the Cluster Transixtor.